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10/20/2005

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Gayle W. Chaney	(Depositor's name)	
Saule W Chaney	(Signature)	
December 21, 2005	(Date)	

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/692,234	10/23/2003	Mark Hollatz	1406/175	8395

TITLE OF INVENTION: METHOD FOR THE PLANARIZATION OF A SEMICONDUCTOR STRUCTURE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE		PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE		
nonprovisional	NO	\$1400		\$300	\$1700	01/20/2006		
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VINH, LAN		1765		438-691000				
CFR 1.363).  Change of correspon Address form PTO/SB/1  "Fee Address" indica PTO/SB/47; Rev 03-02 Number is required.  3. ASSIGNEE NAME ANI	ntion (or "Fee Address" Indic or more recent) attached. Us D RESIDENCE DATA TO E	Correspondence ation form e of a Customer BE PRINTED ON 1	(1) the na or agents (2) the na registered 2 register listed, no		t attorneys  I Jenkin Taylo Taylo as of up to no name is  3	ns, Wilson & or, P.A.		
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a. Applicant claims S	s (from status indicated abov SMALL ENTITY status. See	37 CFR 1.27.		cant is no longer claiming SMAI				
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Authorized Signature	Ohha	10	ent		cember 21, 200			
Typed or printed name	Richard E. J	Jenkins $ee$		Registration	No. 28,428			

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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December 21, 2005



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Gayle W. Chaney Date of Signature

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> U.S. Patent Application Serial No. 10/692,234 for Re:

METHOD FOR THE PLANARIZATION OF A

SEMICONDUCTOR STRUCTURE

Our Ref. No. 1406/175

Enclosed in connection with the above-referenced patent application please find the following: -151. Asia

- Base Issue Fee Transmittal Form (1 page in duplicate); and 1.
- A return-receipt card to be returned to our offices with the U.S. Patent and 2. Trademark filing stamp thereon.

The Commissioner is hereby authorized to charge any fees associated with the filing of this correspondence to Deposit Account No. 50-0426.

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Please contact our offices if there are any questions.

Respectfully submitted,

JENKINS, WILSON & TAYLOR, P.A.

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